Acorbey Docket: 021906-0307405

ference: PIA31178/DBE/US-HO

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE PATENT APPLICATION of:

Confirmation Number: 9617

Application No.: 10/743,454

Group Art Unit: 2823

Filed: December 23, 2003

Examiner: Estrada, Michelle

Title: METHOD FOR PLANARIZING A SURFACE OF A SEMICONDUCTOR WAFER

## **RESPONSE**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 25, 2004, the following Remarks are made.

120.00 DA